SEP 2 9 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

John Elliott

Serial No.:

09/870,461

Filing Date:

June 1, 2001

Group Art Unit:

2124

Examiner:

David H. Malzahan

Title:

STICKY Z BIT

Mail Stop RCE Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450 I hereby certify that this Information Disclosure Statement is being deposited with the United States Postal Service as Express Mail EV351288213US addressed to: Mail Stop RCE, Commissioner of Patents, Alexandria, VA 22313-1450, on the date shown below.

Dear Sir:

INFORMATION DISCLOSURE STATEMENT

Applicant respectfully requests, pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, that the references listed on the attached PTO-1449 form be considered and cited in the examination of the above-identified application. Copies of the references are enclosed on the attached CD-Rom for the convenience of the Examiner. Furthermore, pursuant to 37 C.F.R. §§1.97 (g) and (h), no representation is made that these references are material to the patentability of the present application.

Applicant believes no fees are due with this filing, however, the Commissioner is hereby authorized to charge any fees to Deposit Account No. 50-2148 of Baker Botts L.L.P. in order to effectuate this filing.

Respectfully submitted,

BAKER BOTTS L.L.P. Attorneys for Applicant

Bruce W. Slayden II Reg. No. 33,790

Date: September 27, 2004

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Page 1 of 6 Application No. Applicant(s) PTO-1449 John Elliott 09/870,461 Information Discionus Sitation Group Art Unit Filing Date **Docket Number** in an Application 068354.1470 2124 June 1, 2001 **U.S. PATENT DOCUMENTS CLASS SUBCLASS FILING DATE** DOCUMENT NO. DATE NAME A. 710 260 1/22/73 11/6/73 Cottton et al. 3771146 В. 710 110 10/18/73 3886524 5/27/75 Appelt C. 708 521 3/25/74 5/24/77 Lynch et al. 4025771 Đ. 3/24/76 710 264 4074353 2/14/78 Woods et al. E. 9/30/76 4090250 5/16/78 Carlson et al. 712 234 F. 364 749 10/23/78 4323981 4/6/82 Nakamura G. 11/21/80 708 552 4379338 4/5/83 Nishitani et al. H. 364 200 9/29/80 4408274 10/4/83 Wheatley et al ١. Gerson et al. 200 3/1/82 4451885 5/29/84 708 J. 746 3/25/85 364 4742479 5/3/88 Kloker et al. K. 364 200 8/29/85 4768149 8/30/88 Konopik et al. L. 711 2 4/12/85 4779191 10/18/88 Greenblatt M. 900 12/2/87 364 4800524 1/24/89 Roesgen N. 708 625 3/20/87 6/20/89 Miller et al. 4841468 Ο. 12/28/88 708 628 4926371 5/15/90 Vassilliadis et al. FOREIGN PATENT DOCUMENTS TRANSLATION **SUBCLASS** DOCUMENT NO. DATE COUNTRY CLASS YES NO P. Х 01037424 A 2/89 JP Q. Х ΕP 0 554 917 A2 8/11/93 R. EΡ Х 7/29/98 0 855 643 A1 S. Х 0 992 888 12/4/00 EΡ Т. Х 12/14/00 EP 0 992 889 U. wo Х 96/11443 4/18/96 NON-PATENT DOCUMENTS - DOCUMENT (Including Author, Title, Source, and Pertinent Pages) Moon B I et al.: "A 32-bit RISC Microprocessor with DSP Functionality: Rapid Prototyping" IEICE Transactions on ٧. Fundamentals of Electronics, Communications and Computer Sciences, Institute of Electronics Information and Comm. Eng. Tokyo, JP, vol. E84-A no. 5, pages 1339-1347, XP001060025 ISSN: 0916-8508 5/2001 Turley J: "Balancing Conflicting Requirements When Mixing RISC, DSPs" Computer Design, Pennwell Publ. W. Littleton, Massachusetts, IS, vol. 37, no. 10, pages 46, 48, 50-53, XP000860706 ISSN:0010-4566 10/1998 Levy M: "Microprocessor and DSP Technologies Unite for Embedded Applications" EDN Electrical Design News, X. Cahners Publishing Co., Newtown Massachusetts, US, no. Europe, pages 73-74, 76, 78-80, XP000779113 ISSN: 3/2/1998 0012-7515 **DATE CONSIDERED EXAMINER**

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